

SPECIFICATIONS

Product.	SMPS	Date.	2017.11.16
Model.	FDR480-S	Rev.	1.0
Customer.	Standard	Page.	1

DWG		
CHK		
APPD		

MODEL/CHANNEL		Unit.	24	48	
INPUT	Voltage , Frequency	[V]	AC100 - 240V(AC90 - 264V), 50/60Hz(47 - 63) or DC127~370V (Universal Input)		
	Current	110V	5.0		
		220V	2.5		
	Efficiency	110V	90	92	
		220V	90	92	
	Power factor	110V	0.98 (Io= 100%)		
		220V	0.94 (Io= 100%)		
	Inrush Current	110V	40 (Ta=25℃ , Cold Start)		
220V		80 (Ta=25℃ , Cold Start)			
Leakage Current (Max)	220V	[mA]	2.0mA		
OUTPUT	Nominal Voltage	[V]	24	48	
	Setting Voltage Range	[V]	23.7 ~ 24.3	47.4 ~ 48.6	
	Voltage Adjustment Range	[V]	24 ~ 28	48 ~ 55	
	Current	[A]	20	10	
	Rated Power	[W]	480	480	
	Line Regulations	[mV]	240	480	
	Load Regulations	[mV]	240	480	
	Temperature Drift	[mV]	360	720	
	Ripple& Noise(pk-pk) (*1)	[mV]	300	300	
	Set-up,Rise Time (typ.)	110V	[ms]	3000ms, 150ms (AC110V, Io=100%)	
		220V	[ms]	1500ms, 150ms (AC220V, Io=100%)	
Hold-up Time (typ.)		[ms]	14 (AC IN 220V, Io=100%)		
Function	Over Voltage Protection	[V]	29 ~ 33	56 ~ 65	
	Over Current Protection	[A]	22 ~ 30	11 ~ 15	
	DC_OK Lamp	-	LED(GREEN)		
	DC_LOW Lamp	-	LED(RED)		
	DC_OK Relay contact(max.)	-	60VDC/0.3A, 30VDC/1A, 30VAC/0.5A resistive load		
	Parallel/Series Operation	-	possible (3+1) / possible		
Cooling / O.T.P	-	Convention Cooling			
Electrical Isolation	(1) Input - Output	-	AC 3.0KV 1min, cut-off: 20mA / DC 500V 100MΩ		
	(2) Input - F.G	-	AC 2.0KV 1min, cut-off: 20mA / DC 500V 100MΩ		
	(3) Output - F.G	-	AC 0.5KV 1min, cut-off:100mA / DC 500V 100MΩ		
Environment	Operating temp.&Humidity (*2)	-	-25 ~ 60℃(Refer to "Derating Curve"), 20 ~ 90% RH (Non Condensing)		
	Storage temp. & Humidity	-	-40 ~ 85℃, 20 ~ 90% RH (Non Condensing)		
	Vibration	-	10~55Hz at 2G 1minutes period, 1hour along X,Y and Z axis		
Dimension	Size(WxHxD) / Weight	mm / g	85×130×125	/ 1700	
Safety	SAFETY REGULATION	-	cUL,CE,CB	cUL,CE,CB	
Emission	LINE CONDUCTED RF VOLTAGE	-	Complied with EN55011/EN55022-B, FCC-B		
PFHC	Harmonic Current	-	Complied with EN61000-3-2 (Class-A)		

(*1)Terminated with 0.1uF & 47uF parallel capacitor(Bandwidth=20MHz), (Ta=0℃~60℃)

(*2)Installation clearances : 40mm on top, 20mm on bottom, 5mm on the left and right side are recommended when loaded permanently with full power. In case the adjacent device is a heat source, 15mm clearance is recommended.